

ABSTRACT OF THE DISCLOSURE

A control chip, a circuit and a method thereof with bus cycle inhibiting function is provided. A bus resource decode circuit is used to determine if a bus cycle picked up from a first bus is an internal bus cycle type of the control chip. If the bus cycle is
5 found to be an internal bus cycle, an inhibit signal is transmitted to a bus bridging circuit after a logic computation inside a logic circuit so that a re-transmission of the internal bus cycle to a second bus is inhibited. In this manner, the second bus may step into an idle state and save some electrical power.